

In the Claims:

Please amend claims 1-8, 9-17 and 19-20. The claims are as follows:

1. (Currently Amended) A method of generating a set of scan diagnostic patterns for diagnosing fails in a set of scan chains comprised of scan chain latches, comprising:

(a) selecting a group of scan chain latches from said set of scan chains, said group of scan chain latches including all or some of said scan chain latches;

(b) selecting a pattern from the a set of test patterns;

(c) determining the a number of lateral insertions of said ~~selected~~ pattern selected from said set of test patterns;

(d) determining a number of new lateral insertions that said ~~selected~~ pattern selected from said set of test patterns would add to said set of scan diagnostic pattern and adding said ~~selected~~ pattern selected from said set of test patterns and a corresponding new insertion count to a count list;

(e) repeating steps (b) through (d) until all patterns of said set of test patterns have been selected;

(f) selecting a pattern from said count list;

(g) adding said pattern selected from said count list to said set of scan diagnostic patterns;

and

(h) repeating steps (b) through (g) until [[a]] there are a predetermined number of patterns in said set of scan diagnostic patterns.

2. (Currently Amended) The method of claim 1, wherein step (f) selects a pattern from said count list with the a highest number of lateral insertions.

3. (Currently Amended) The method of claim 1, wherein step (f) selects a pattern from said count list that will add the a highest number of new lateral insertions to said set of scan diagnostic patterns.

4. (Currently Amended) The method of claim 1, wherein step (c) includes comparing said selected pattern selected from said set of test patterns to a resultant pattern generated by a simulated load/unload sequence of said set of scan chains using said selected pattern selected from said set of test patterns to load said set of scan chains.

5. (Currently Amended) The method of claim 1, wherein:

step (d) includes determining said number of new lateral insertions based on the number of new lateral insertions that said selected pattern selected from said set of test patterns would add to a mark off list table containing all lateral insertions of all patterns in said set of scan diagnostic patterns; and

step (g) includes adding said new lateral insertions of said pattern selected from said count list to said mark off table.

6. (Currently Amended) The method of claim 1, wherein step (d) determines the number of new lateral insertions based on lateral insertions of said selected pattern selected from said set of test

patterns that change the contents of a latch in said set of scan chains from a logical zero to a logical one or from a logical one to a logical zero.

7. (Currently Amended) The method of claim 1, wherein step (d) does not determine the number of new lateral insertions based on lateral insertions of said ~~selected~~ pattern selected from said set of test patterns that do not change the logical contents of a latch in said set of scan chains.

8. (Original) The method of claim 1, wherein said set of scan diagnostic patterns include patterns having lateral insertions that insert a logical zero in latches of said scan chains already containing a logical zero and that that insert a logical one in latches of said scan chains already containing a logical one.

9. (Currently Amended) The method of claim 1, wherein step (b) includes not selecting patterns from said set of test patterns that have been previously added to said set of scan diagnostic patterns and not selecting patterns from said set of test patterns that shift scan chains.

10. (Currently Amended) The method of claim 1, wherein said set of test patterns is a set of fault patterns.

11. (Currently Amended) A computer system comprising a processor, an address/data bus coupled to said processor, and a computer-readable memory unit adapted to be coupled to said processor, said memory unit containing instructions that when executed by said processor implement a method of generating a set of scan diagnostic patterns for diagnosing fails in a set of

scan chains comprised of scan chain latches, comprising ~~the computer implemented a computer~~
to implement a method comprising steps of:

(a) selecting a group of scan chain latches from said set of scan chains, said group of scan chain latches including all or some of said scan chain latches

(b) selecting a pattern from a set of test patterns;

(c) determining the a number of lateral insertions of said ~~selected~~ pattern selected from said set of test patterns;

(d) determining a number of new lateral insertions that said selected pattern would add to said set of scan diagnostic pattern and adding said selected pattern and a corresponding new insertion count to a count list;

(e) repeating steps (b) through (d) until all patterns of said set of test patterns have been selected;

(f) selecting a pattern from said count list;

(g) adding said pattern selected from said count list to said set of scan diagnostic patterns;
and

(h) repeating steps (b) through (g) until ~~[[a]]~~ there are a predetermined number of patterns in said set of scan diagnostic patterns.

12. (Currently Amended) The system of claim 11, wherein method step (f) selects a pattern from said count list with ~~the~~ a highest number of lateral insertions.

13. (Currently Amended) The system of claim 11, wherein method step (f) selects a pattern from said count list that will add the a highest number of new lateral insertions to said set of scan diagnostic patterns.

14. (Currently Amended) The system of claim 11, wherein method step (c) includes comparing said ~~selected~~ pattern selected from said set of test patterns to a resultant pattern generated by a simulated load/unload sequence of said set of scan chains using said ~~selected~~ pattern selected from said set of test patterns to load said set of scan chains.

15. (Currently Amended) The system of claim 11, wherein

method step (d) includes determining said number of new lateral insertions based on the number of new lateral insertions that said ~~selected~~ pattern selected from said set of test patterns would add to a mark off list table containing all lateral insertions of all patterns in said set of scan diagnostic patterns; and

method step (g) includes adding said new lateral insertions of said pattern selected from said count list to said mark off table.

16. (Currently Amended) The system of claim 11, wherein method step (d) determines the number of new lateral insertions based on lateral insertions of said ~~selected~~ pattern selected from said set of test patterns that change the contents of a latch in said set of scan chains from a logical zero to a logical one or from a logical one to a logical zero.

17. (Currently Amended) The system of claim 11, wherein method step (d) does not determine the number of new lateral insertions based on lateral insertions of said ~~selected~~ pattern selected from said set of test patterns that do not change the logical contents of a latch in said set of scan chains.

18. (Original) The system of claim 11, wherein said set of scan diagnostic patterns include patterns having lateral insertions that insert a logical zero in latches of said scan chains already containing a logical zero and that that insert a logical one in latches of said scan chains already containing a logical one.

19. (Currently Amended) The system of claim 11, wherein step (b) includes not selecting patterns from said set of test patterns that have been previously added to said set of scan diagnostic patterns and not selecting patterns from said set of test patterns that shift scan chains.

20. (Currently Amended) The system of claim 11, wherein said set of test patterns is a set of fault patterns.